Intriguing conducting properties of HfO$_x$N$_y$ thin films prepared from the Hf[N(C$_2$H$_5$)$_2$]$_4$

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Hafnium nitride films were prepared on the Si(100) substrates by the metal-organic chemical vapor deposition method using Hf[N(C$_2$H$_5$)$_2$]$_4$. The prepared samples were then oxidized in air, followed by rapid-thermal annealing to produce HfO$_x$N$_y$ thin films, meanwhile the associated physical properties were investigated. The x-ray photoelectron spectroscopy analysis unveiled that the composition of the films is HfO$_x$N$_y$. In addition, the films after the rapid-thermal annealing treatments at various temperatures revealed salient features in their physical properties, such as capacitance and conductivity. On this basis, the feasibility of using the HfO$_x$N$_y$ layers as high-k dielectrics in complementary metal oxide semiconductor transistors was also discussed. © 2006 American Institute of Physics. [DOI: 10.1063/1.2405848]
clearly indicates that oxidation of the films by RTA under a nitrogen atmosphere was not efficient at higher temperatures because escaping of dangling O was enhanced.

Field emission scanning electron microscope images (not shown here) of the films annealed at various temperatures indicate that the particle size increases with increasing annealing temperature. X-ray diffraction (XRD) data shown in Fig. 2 also suggest that the crystallinity increases as the RTA temperature is raised. The parallel increases of inhomogeneous size distribution and degree of crystallization suggest that mobility and degrees of freedom of the surface particles increased during nucleation at high temperatures. For a fair comparison, an XRD pattern of the HfO$_2$ thin film prepared via RTA at 773 K is also shown at the top of Fig. 2. Obviously, the HfO$_2$ sample reveals a more resolved pattern, pared via RTA at 773 K is also shown at the top of Fig. 2. The results clearly demonstrate that incorporation of both N and O atoms into the films greatly impairs the crystallization process, in which RTA temperature also plays a crucial role. How this affects the electrical properties of the films is discussed below.

MOS capacitors with HfO$_N_2$ dielectrics prepared at different RTA temperatures were fabricated. Thickness of the as-deposited, the RTA processed at 773 K, and the one annealed at 1223 K was determined with an N&K analyzer to be 116, 102, and 51 nm, respectively. Typical C-V and I-V curves of the devices, without any hysteresis, are shown in Fig. 3.

Capacitance of the layer annealed at 1223 K reveals the lowest flatband voltage at $V_{fb}=0.2$ V while both of the as-deposited and the 773 K annealed samples display the voltages at 0.7 V. The result is similar to that observed in silicon oxynitride devices and can thus be rationalized by formation of localized positive charges at high annealing temperatures. It should be noted that a low leakage current is observed for the as-deposited thin film while variable leakage values are seen for the annealed samples. The samples annealed at 873–1073 K were metallic as shown in a resistant versus annealing temperature plot (not displayed here). These samples are ignored for the dielectric value estimation. The $k$ values are measured to be 9 and 42 for the samples annealed at 773 and 1223 K, respectively. In addition to other factors, a significant thickness shrinking at 1223 K may contribute to the increased $k$ value. For comparison, the $k$ value of HfO$_2$ is 25.3.

Another salient feature is the electric conductivity, which, measured with a four-point probe, was distributed from a range of conductorlike to insulation (not shown here). The leakage current of thin films annealed at 873–1123 K exhibits mixed behavior of conductorlike and semiconductor properties, while the films prepared at as deposited, 773 and 1223 K, are insulatorlike. To gain detailed insight into the associated electrical properties, the metal-like behavior film prepared at 973 K via RTA was selected to gauge the changes of resistance as a function of temperature. Figure 4 depicts a plot of resistance ($R$) versus temperatures ($T$) for a HfO$_N_2$ thin film prepared via RTA at 973 K. Of particular interest is the appearance of a rise and a decay component in the temperature range of 310–630 K. The rise component appears to be linear at 310–430 K and can be well fitted by $R=190+0.82T$, indicating a conductorlike property, while the decay follows first order exponential behavior and can be fitted with $R=5+0.0016e^{-5200T}$, a typical property of the semiconductor. The interplay between these two properties gives rise to a peak temperature at 430 K. The reproducibility and reversibility of the results eliminate any artifacts caused by the instrumentation. In addition, purging N$_2$ during $R$-$T$ measurement has negligible effect on the $R$-$T$ plot. Accordingly, the oxidation reaction, due to trace amount of
oxygen existing in the vacuum system, can also be discarded. Thus, the peak around 430 K may originate from either phase transition or a subtle temperature-dependent variation of the films. The former case is very unlikely since the phase transition temperature is expected to be well above 340 K for the HfO,N film. In fact, that HfO,N is not a stoichiometric compound, to a certain extent, and can have local variations in O and N contents, despite the global homogeneous measured experimentally. Thus it is possible have conductive fine lines embedded in a semiconductor matrices. Assuming that the density of the conductive lines is greater than the percolation threshold, then the conductivity of the sample is a sum of both as shown below:

$$\sigma = \alpha_{\text{metal}} + \alpha_{\text{semi}}.$$  

(1)

Evidently, $\alpha_{\text{metal}}$ is proportional to $1/T$ and $\alpha_{\text{semi}}$ is proportional to $e^{-\frac{T_0}{T}}$ according to simple Drude model.\textsuperscript{17} It is also noticeable that, as supported by the steep slope of the plot within 310 and 430 K, most areas of the films are believed to be semiconductorlike.

As the temperature was decreased to below room temperature, the plot of $R$ vs $T$ remains a linear behavior down to 150 K, followed by a rapid rising with an $R/T$ relationship of $R \sim 265e^{(50/T)^{2}}$ (see inset of Fig. 4). The results can be tentatively rationalized by a mechanism incorporating electron trapping. Due to the inhomogeneous distribution of composition, there exist certain defects among the conducting fine lines to trap electrons. As temperature is low enough (i.e., $<150$ K) that trapped electrons have insufficient thermal energy to escape, and the resulting conductivity is accordingly reduced drastically. Furthermore, one cannot eliminate the possibility of the occurrence of certain local phase transitions at $\sim 150$ K, which may also play a role in impeding the conductivity. Work focusing on this is currently in progress and the results will be published in a separate issue.

In conclusion, we report the intriguing, previously unrecognized physical properties of the high dielectric material, HfO$_x$N$_y$, prepared from a single precursor Hf[N(C$_2$H$_3$)$_2$)$_4$, via MOCVD followed by controlled oxidation and RTA steps. Despite HfO$_2$ thin films being the most noticeable material in the preparation of the high dielectric films, HfO$_x$N$_y$ prepared via the steps described in this study may furnish a potential material because of its strong inhibition to crystalization as well as the great enhancement in capacitance.\textsuperscript{18} Furthermore, in potential high-$k$ device applications, the advantage of HfO,$_x$N$_y$ over HfO$_2$ lies in its physically greater thickness under the same equivalent oxide thickness. We thus believe that the prepared films may provide a stable interface layer with a high dielectric constant. This, in combination with the intriguing physical properties elaborated above, may spark a broad spectrum of interest in view of possible applications.

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