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Low temperature improvement on silicon oxide grown by electron-gun evaporation for resistance memory applications

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In this work, the supercritical CO2 fluid mixed with cosolvents is introduced to terminate the traps in electron-gun (e-gun) evaporation deposited silicon oxide (SiOx) film at 150 °C. After the proposed treatment, the SiOx film exhibits a lower leakage current and a resistive switching behavior that is controllable by applying proper voltage bias. The change in resistance is over 102 times and the retention time attains to 2 × 105 s. It is also discovered that the resistive switching behavior seemingly relates to the amount of traps. © 2008 American Institute of Physics.

Since the first observation of bistable resistance states in the 1960s, reversible and reproducible resistive switching phenomena caused by applied electric field have been investigated widely to be used as resistive random access memories (RRAMs). In recent years, many metal oxides and perovskite oxides, such as Nb2O5, TiO2, and Nb-doped SrTiO3, had been reported for RRAM applications.1–3 Nevertheless, there are only a few studies mentioning the process of producing RRAM at low temperature. In this study, the supercritical fluid (SCF) technology is introduced to fabricate resistive switching memory at 150 °C. The SCF is usually utilized to extract impurity, dehydrate, and dry materials with no damage.4–6 It also has been applied to deliver oxidant into a metal oxide film to terminate electrical traps.7,8 Due to the origin of bistable resistance states possibly caused by some kind of charge traps,9,10 we would employ SCF technology to make the e-gun deposited oxide own bistable resistance states by varying the amount of traps.

The SiOx films with an average thickness of 5–7 nm were directly deposited on p-type silicon wafers by e-gun evaporation system using a pure SiO2 target. The chamber pressure and the substrate temperature were maintained at 2 × 10−6 torr and 25 °C, respectively. These SiOx films were split into three groups and treated with different post-treatments. The first group was placed in a SCF system at 150 °C for 2 h, where it was full of 3000 psi supercritical CO2 (SCCO2) fluid. The second group was immersed into a pure H2O vapor ambience at 150 °C for 2 h, in a pressure-proof stainless steel chamber. The third group was treated with pure 3000 psi SCCO2 fluid; the bias voltage (Vb) was applied on the top electrode with a grounded bottom electrode. The plot of ln(J/E2) versus the reciprocal of electric field (1/E) for the current density in negative bias condition is displayed in the top right inset of Fig. 1. A linear dependence indicates that the trap-assisted tunneling (as the schematic band diagram in the bottom left inset of Fig. 1) dominated conduction mechanism while the negative bias was larger than ~0.8 V.11 The high leakage current reaching about 10−1 A/cm2 implies that numerous traps are present in the e-gun deposited SiOx film. In positive bias condition, the Al bottom electrode was deposited onto the backside of p-type silicon wafers for the enhancement of voltage coupling. The electrical characteristics of MIS structure were measured by a HP 4156-A semiconductor analyzer.

Figure 1 shows the current density (J) of SiOx film that was treated with pure 3000 psi SCCO2 fluid; the bias voltage (Vb) was applied on the top electrode with a grounded bottom electrode. The plot of ln(J/E2) versus the reciprocal of electric field (1/E) for the current density in negative bias condition is displayed in the top right inset of Fig. 1. A linear dependence indicates that the trap-assisted tunneling (as the schematic band diagram in the bottom left inset of Fig. 1) dominated conduction mechanism while the negative bias was larger than ~0.8 V.11 The high leakage current reaching about 10−1 A/cm2 implies that numerous traps are present in the e-gun deposited SiOx film. In positive bias condition, the

![FIG. 1. Plot of current density (J) vs bias voltage for the SiOx film that was treated with 3000 psi SCCO2 fluid. Insets: (top right) the curve of ln(J/E2) vs (1/E) for the current density in negative bias condition, and (bottom left) the schematic band diagram of trap-assisted tunneling in negative bias condition, (bottom right) the schematic band diagram accounting for leakage current generated from interface states in positive bias condition.](image-url)
electrons originate commonly from (1) the interface states, (2) traps in depletion region, and (3) bottom electrode of substrate.\textsuperscript{12} In this work, the generation of electrons from the bottom electrode of substrate or the traps in the depletion region is negligible because the substrate is \textit{p}-type single-crystal Si. Therefore, the saturate-like leakage current was generated mainly from the interface states as illustrated in the bottom right inset of Fig. 1, and it was limited by the density of interface states and the carrier generation rate. This limitation caused a lower leakage current in positive bias condition. From these results, we revealed that the pure CO\textsubscript{2} molecule is ineffective to passivate traps.

The current curves of the H\textsubscript{2}O vapor treated SiO\textsubscript{x} film are shown in Fig. 2(a), and it expressed the high resistance state initially. Interestingly, this SiO\textsubscript{x} film was detected to exhibit a resistive switching behavior between high resistance state ($R_{HI}$) and low resistance state ($R_{LI}$), and the resistance state is variable by applied bias voltage. The voltages of switching resistance state are about 1.9 (from $R_{HI}$ to $R_{LI}$) and -1.8 V (from $R_{LI}$ to $R_{HI}$). The maximum ratio of the two resistance states ($R_{HI}/R_{LI}$) is over $10^2$ times, and the retention property of the $R_{HI}$ state at room temperature is displayed in the inset of Fig. 2(a).

For the pure SCCO\textsubscript{2} fluid treated SiO\textsubscript{x} film. Additionally, the leakage current was reduced obviously after H\textsubscript{2}O vapor treatment. For realizing the reduction of leakage current, the current density of the high resistance state in negative bias condition was analyzed according to Poole–Frenkel (PF) emission, shown in Fig. 2(b). The linear trend indicates that the PF emission dominated conduction mechanism. The PF emission is owing to field enhanced thermal excitation of trapped electrons in insulator onto the conduction band.\textsuperscript{13} The conversion of conduction mechanism from trap-assisted tunneling to PF emission demonstrates the reduction of traps, and it is attributed to the parts of traps in the SiO\textsubscript{x} film that had been passivated by H\textsubscript{2}O molecule during the H\textsubscript{2}O vapor process.\textsuperscript{14}

Figure 3(a) shows the current curves of the SiO\textsubscript{x} film that was treated with cosolvent added 3000 psi SCCO\textsubscript{2} fluid. After this treatment, the SiO\textsubscript{x} film also performed a resistive switching behavior with higher voltages of switching resistance state and longer retention time. Figure 3(b) shows the plot of $\ln(J/E)$ versus square root of applied electric field ($E^{1/2}$) for the current density of the high resistance state in negative bias condition. The inset shows the schematic band diagram including the SR emission (at low electric field) and PF emission (at high electric field).
negative bias condition. The two-slope linear dependence indicates that the conduction mechanism was dominated by Schottky–Richardson (SR) emission at low electric field and then dominated by PF emission while the negative bias was larger than ~2.4 V. The SR emission is independent of traps and caused by electron exciting thermally across the potential energy barrier via field assisted lowering at a metal–insulator interface. The low leakage current of 10^{-7} A/cm^{2} expresses that the traps were further terminated, i.e., the SCCO_{2} fluid owns superior capability of delivering H_{2}O molecule into SiO\textsubscript{x} layer than H_{2}O vapor. Although the PF emission occurred at high electric field, a few traps remained, but the proposed SCCO\textsubscript{2} treatment is still sufficient to improve the quality of the SiO\textsubscript{x} film.

From the above results, the resistive switching behavior is apparently associated with the amount of traps. The authors infer that the resistance variation perhaps resulted from the movement of carrier in the SiO\textsubscript{x} gap, and the possible diagram mechanisms are shown in Figs. 4(a) and 4(b). Applying a positive bias enough to cause the tunneling of electrons in the SiO\textsubscript{x} gap, the energy band diagram would twist and lead to the change of resistance state from $R_{H}$ to $R_{L}$. Applying a negative bias enough to make these electrons tunnel back, the resistance state would return from $R_{L}$ to $R_{H}$.

If there are numerous traps present in the SiO\textsubscript{x} gap, such as a pure SCCO\textsubscript{2} fluid treated sample, these electrons might tunnel back without external bias, and thereby no change in resistance is observed. On the other hand, if there are only a few traps present in the SiO\textsubscript{x} gap, it is required to apply a higher bias to shift the electrons, and a superior retention is expected. Thus, in comparison with H\textsubscript{2}O vapor treated sample, the SiO\textsubscript{x} film treated with cosolvent added SCCO\textsubscript{2} fluid performs higher bias voltages of switching resistance state and longer retention time.

In summary, the preliminary improvement on the e-gun deposited SiO\textsubscript{x} film was obtained after H\textsubscript{2}O vapor treatment as a result of passivating traps by H\textsubscript{2}O molecules. A further study also demonstrated that the trap passivation efficiency is optimized by the treatment of the SCCO\textsubscript{2} fluid mixed with ethyl alcohol and H\textsubscript{2}O, because SCCO\textsubscript{2} fluid could more effectively carry H\textsubscript{2}O molecule into the SiO\textsubscript{x} layer. Besides, the reduction of traps would induce a reversible resistive switching behavior. This phenomenon possibly resulted from the movement of carrier in the SiO\textsubscript{x} gap, so the amount of traps would influence the bias voltages of switching resistance state and the retention time. By the proposed SCCO\textsubscript{2} technology, it is promising for e-gun deposited SiO\textsubscript{x} to achieve a full low temperature fabrication of resistive memory.

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**References**