Abstract—In this paper, we consider the scheduling problem in time-division multiplexed (TDM) switching systems. In previous works, the interdependence between traffic demands in two consecutive frames is neglected, and scheduling algorithms found up to now have time complexities $O(N^3)$ or $O(N^{4.5})$, where $N$ is the switch size. However, in many applications like voice or video communications, if a source transmits a packet to a destination in a frame, it is highly probable that it will also transmit a packet to the same destination in the next frame. So it is not necessary to schedule incoming packets for every frame if we can preserve all the switching patterns for the nearest scheduled frame and update the patterns accordingly according to the changes of traffic demands. The adaptive algorithm proposed in this paper assigns time slots to packets according to the changes of traffic demands. This algorithm has the worst case time complexity $O(N^2L)$, where $L$ is the TDM frame length. Comparing the time complexity of the adaptive algorithm with those of previous scheduling algorithms, the adaptive algorithm can perform better than previous scheduling algorithms when $N$ is large and/or $L$ is small. Since traffic demands in consecutive frames are expected to be interdependent in many applications, the proposed algorithm may offer as an efficient alternative for scheduling time slots in these applications.

I. INTRODUCTION

Time-division multiplexed (TDM) switching has widely been employed in satellite communications [23] and terrestrial networks [14]. In terrestrial networks, a time-division packet switching system may have a switch in which the internal switch elements are set to establish paths from input lines to output lines. In a satellite-switched time-division multiple access (SS/TDMA) system, the satellite has several spot-beam antennas and a solid-state RF switch. The antennas are directed toward spatially disjoint geographical zones and the switch provides connections from up-link beams to down-link beams through on-board transponders. Traffic from an earth station in the beam zone covered by an up-link beam is routed by a transponder to an earth station in the beam zone covered by a down-link beam.

Fig. 1. An $N \times N$ switching system.

In both cases, the TDM switching system can be represented as shown in Fig. 1. There are $N$ input lines and $N$ output lines, where $N$ is referred to as the switch size. Traffic in a TDM line is assumed to be allocated in repetitive frames of which each has $L$ time slots, where $L$ is referred to as the TDM frame length or the capacity of a TDM line. Time slots are synchronized and shared by users. Sources at the inputs can communicate with destinations at the outputs by asking for different amount of time slots to transmit packets to destinations. Each new traffic demand can be accepted if the input line and the output line both have enough free time slots. There are two kinds of possible traffic conflicts in a TDM switching system; namely internal conflicts and output conflicts. If more than one packet are routed through the same internal link of the switch in the same time slot, an internal conflict occurs. If more than one packet are destined for the same output line in the same time slot, an output conflict occurs. Both kinds of traffic conflicts may result in loss of packets. In this paper, we assume that the switch is a nonblocking switch and therefore there are no internal conflicts [7]. We shall consider the scheduling problem to avoid output conflicts. Generally connection requests arise without coordination, so it is necessary to ensure that incoming packets from different TDM input lines should not be destined for the same output line in the same time slot. The task to scheduling packets is performed by the network controller to avoid conflicts at the outputs. Therefore each source conducts its TDM transmission according to the time slot assignment announced by the network controller.

The scheduling problem to avoid output conflicts can be solved by using the time slot assignment algorithms developed in [1]-[4], [8], [13], [17], [19]-[21]. These time slot assignment algorithms are essentially to find a sequence of switching configurations to transmit packets in each frame with the objective that the overall transmission duration is minimum. A switching configuration is a set of switch settings by which packets can be transmitted without conflicts. These algorithms find a switching configuration by
calling some combinatorial optimization algorithms, such as algorithms for finding a system of distinct representative (SDR) [13], [20], [21], finding a max-min bipartite matching [2], [3], [8], finding a maximum cardinality matching [17], and finding a maximum weight matching [19] etc. The number of switching configurations needed in each frame is at most \( O(N^2) \) [2]-[4], [13], [19], [20], and these combinatorial algorithms are of time complexities \( O(N^2) \) [16] or \( O(N^{2.5}) \) [11]. So the time complexities of these time slot assignment algorithms are \( O(N^2) \) or \( O(N^{4.5}) \). Although they are polynomial time algorithms, their time complexities are still so high that they are not very suitable in many practical applications. The computational bottleneck may be overcome by using special-purpose parallel processors to reduce the time spent in finding a switching configuration [21]. However, the number of processors increases with \( O(N^2) \). This may incur extra cost of the switch in a TDM system. In this paper, we shall propose a scheduling algorithm with a lower time complexity.

In previous works [1]-[4], [6], [8]-[10], [13], [17], [19]-[21], the interdependence between traffic demands in two consecutive frames is neglected. However, in many practical applications, if a source transmits a packet to a destination in a frame, it is highly probable that it will also transmit a packet to the same destination in the next frame unless the connection from the source to the destination is no longer needed. For example, consider the video communications which have the following characteristics:

- Packets in video communications are generated at their sources at a constant rate.
- Because of the real-time requirement, a video packet is considered to have a higher priority than a data packet has. In general, packets of lower priorities are taken as space fillers in the transmission of packets of higher priorities.
- Each video connection usually continues for a number of consecutive frames.

With these characteristics, traffic demands between two consecutive frames is correlated. So it is not necessary to schedule incoming packets for every frame if we can preserve all the switching patterns for the nearest scheduled frame and update the patterns appropriately according to the changes of traffic demands.

In this paper, we shall give an adaptive algorithm which assigns time slots to packets according to the changes of traffic demands. The amount of changes of traffic demands will therefore affect the performance of the adaptive algorithm. It is shown that the adaptive scheduling algorithm will be faster than previous time slot assignment algorithms with interdependent traffic since the amount of changes of traffic demands between any two consecutive frames may be small.

In the following, we shall present the mathematical formulation of the scheduling problem in Section II. The adaptive scheduling algorithm will be proposed in Section III. A probability model with interdependent traffic is used to analyze the amount of changes of traffic demands in Section IV. Also included in Section IV are computer simulation results with interdependent traffic. Some conclusions are made in Section V.

II. PROBLEM FORMULATION

The system under consideration is shown in Fig. 1 with an \( N \times N \) nonblocking switch. Each input line is allowed \( L \) time slots per frame to transmit packets to destinations at the outputs, and each output line is also allowed \( L \) time slots per frame to receive packets from sources at the inputs. The traffic demands in a frame can be characterized by an \( N \times N \) traffic matrix \( T \). An entry \( t_{ij} \) in \( T \) represents the traffic demands from input \( i \) to output \( j \) in the frame, measured in number of packets. We assume that exactly one packet can be transmitted in a time slot. Therefore \( t_{ij} \) also represents the number of time slots needed to transmit packets from input \( i \) to output \( j \) in the frame. A switching configuration in the \( r \)-th time slot can be characterized by an \( N \times N \) switching matrix \( S_r \). An entry \( s_{r ij} \in S_r \) represents the source at input \( i \) can transmit a packet in the \( r \)-th time slot to the destination at output \( j \) if the switching pattern is set according to \( S_r \) in the \( r \)-th time slot. Hence a packet from input \( i \) to output \( j \), denoted as \( P_{ij} \), is said to be allocated in the \( r \)-th time slot, or to be allocated in \( S_r \). The \( r \)-th time slot is said to be assigned to or reserved for packets corresponding to those unities in \( S_r \). Since it is impossible for a source to transmit more than one packet in a time slot, there is no more than one unity in a row of a switching matrix. In addition, there is no more than one unity in a column of a switching matrix, otherwise, output conflicts may occur at the output corresponding to this column. So it is necessary for a switching matrix \( S_r \) to have exactly one unity in each row and each column. All the other entries are 0's.

Given an \( N \times N \) traffic matrix \( T \), we call a row or a column a line (line sum). The sum of entries in a line is called the line sum. The line sum of a row \( i \) (column \( j \), denoted by \( R_i \), \( C_j \), is called a row (column) sum, where \( 1 \leq i \leq N \) (\( 1 \leq j \leq N \)). Since a new traffic demand from input \( i \) to output \( j \) can be accepted if the frames at input \( i \) and output \( j \) have enough free time slots,

1. the number of packets transmitted by a given input \( i \) can not exceed \( L \) in a given frame, i.e., \( R_i \leq L \), and
2. the number of packets destined for a particular output \( j \) can not exceed \( L \) in a given frame, i.e., \( C_j \leq L \).

The above conditions are called the scheduling criteria [1], [21], [22]. It has been shown that if \( T \) satisfies the above scheduling criteria then it is possible to schedule incoming packets such that no conflicts occur [1]. The scheduling problem is then defined as follows: Given a traffic matrix \( T \) which satisfies the scheduling criteria, find a sequence of \( L \) switching matrices \( \{S_r\} \) such that each packet of the traffic matrix \( T \) can be allocated in an \( S_r \), i.e., \( \sum_{r=1}^{L} s_{r ij} \geq t_{ij} \) for all \( i \) and \( j, 1 \leq i, j \leq N \). The sequence \( \{S_r\} \) is called a schedule of \( T \).
As mentioned earlier, in many practical applications, traffic demands between two consecutive frames are interdependent. Most of traffic demands in a given frame may be the same as those in the frame coming before it. If we can preserve all the switching patterns for the nearest scheduled frame and update the switching patterns appropriately according to the changes of traffic demands, it is expected that the scheduling time can be reduced. Based on this concept, we define the adaptive scheduling problem as follows: Given a traffic matrix \( T \) which satisfies the scheduling criteria and a sequence of \( L \) switching matrices \( \{ S'_r \} \), find switching matrices \( \{ S \} \) by updating \( \{ S'_r \} \) such that the sequence \( \{ S \} \) is a scheduling of \( T \).

An \( N \times N \) traffic matrix \( T \) is said to be complete with respect to \( L \) or complete if all the line sums of \( T \) are equal to \( L \). That is, \( T \) is complete if \( R_1 = \ldots = R_N = C_1 = \ldots = C_N = L \). Note that a complete traffic matrix satisfies the scheduling criteria. Also we can not add any new traffic demand to a complete traffic matrix; otherwise, the scheduling criteria is violated. It has been shown that the sequence \( \{ S \} \) is called an optimal schedule of \( T \) if and only if \( T = \sum_{r=1}^{L} S_r \), i.e., \( \sum_{r=1}^{L} s_{r,j} = i_j \) for all \( i \) and \( j \). \( 1 \leq i, j \leq N \) \([1],[2],[4],[13]\). The sequence \( \{ S \} \) is an optimal schedule of the complete traffic matrix \( T \).

It has been shown that, given any traffic matrix \( T \) which satisfies the scheduling criteria, it is always possible to add appropriate non-negative integers (called dummy traffic) to the entries in \( T \) such that the resulting traffic matrix, denoted as \( T' \), is complete with respect to \( L \) \([1],[2],[4],[13]\). An optimal schedule of \( T' \) is obviously a schedule of \( T \) \([1],[2],[4],[13]\). So the adaptive scheduling problem with a traffic matrix \( T \) can be solved if the adaptive scheduling problem with the corresponding traffic matrix \( T' \) is solved. In this paper, we shall follow this approach to find a schedule of \( T \) by finding an optimal schedule of \( T' \). The assignments corresponding to the dummy traffic can be ignored, and simply reflect the fact that the available capacity of the switching system exceeds the total accepted traffic demands.

### III. Algorithm

In this section, we shall first describe the basic idea behind the adaptive scheduling (AS) algorithm by showing how to adaptively assign time slots to packets according to the changes of traffic demands. The algorithm is then given, together with the analysis of its time complexity.

#### A. Basic Idea

We first introduce some notations to be used in the AS algorithm. If an entry \( x_{ij} \) of a matrix is equal to \( 1, 0, \) or \( -1 \), we denote it by \( 1_{ij}, 0_{ij}, \) or \( -1_{ij}, \) respectively. If there is only one non-zero entry \( 1_{ij} \) or \( -1_{ij} \) in a matrix, we denote this matrix by \( [1]_{ij} \) or \( [-1]_{ij} \), respectively.

Given a schedule \( \{ S'_r \} \) for a frame and a traffic matrix \( T \) which satisfies the scheduling criteria, we want to find a schedule \( \{ S \} \) of \( T \) by updating \( \{ S'_r \} \). Let \( T' = \sum_{r=1}^{L} S'_r \). Then \( T' \) is a complete traffic matrix corresponding to the scheduled frame and \( \{ S'_r \} \) is an optimal schedule of \( T' \).

First, we add dummy traffic to \( T' \) such that the resulting traffic matrix \( T'' \) is complete with respect to \( L \). Then we initiate the matrix \( V \) with \( T'' = T' \). If we can update the switching matrices \( \{ S'_r \} \) such that \( V \) becomes a zero matrix, the resulting sequence of switching matrices \( \{ S \} \) is an optimal schedule of \( T'' \) and hence a schedule of \( T \).

We observe that each line sum of \( V \) is zero. This is because each line sum of \( T'' \) and \( T' \) is equal to \( L \). If there exists a negative entry \( v_{ij} \) for some \( i \) and \( j \), then there exists a positive entry \( v_{ij} \) for some \( q \) since the \( i \)-th row sum of \( V \) is zero. Similarly, there is also a negative entry \( v_{ij} \) for some \( p \) since the \( q \)-th column sum of \( V \) is zero. The positive entry \( v_{ij} \) indicates that there are \( v_{iq} \) new traffic demands, measured in number of packets, from input \( i \) to output \( q \) needed to be allocated in the current frame. The negative entry \( v_{ij} \) indicates that there are \( |v_{ij}| \) time slots which are assigned in the previously scheduled frame to traffic demands from input \( i \) to output \( j \) and no longer needed in the current frame. So they can be deallocated. Similarly, the negative entry \( v_{pq} \) indicates that there are \( |v_{pq}| \) time slots which are assigned in the previously scheduled frame to traffic demands from input \( p \) to output \( q \) and no longer needed in the current frame. Also they can be deallocated. Hence there exists a switching matrix \( S'_r \) containing an entry \( 1_{ij} \), and there exists a switching matrix \( S'_r \) containing an entry \( 1_{pq} \). Then, by calling the reallocation algorithm described in the following, we shall deallocate \( P_{ij} \) from the \( x \)-th time slot and \( P_{ij} \) from the \( y \)-th time slot, and allocate \( P_{ij} \) in the \( y \)-th time slot. Then \( S'_r \) and \( S'_r \) are subsequently updated such that (1) the resulting matrices \( S_r \) and \( S_r \) are still switching matrices, and (2) \( S_r + S'_r - S'_r - S'_r = [1]_{ij} + [-1]_{ij} + [1]_{pq} + [1]_{pq} \). The last expression means that we let time slots originally assigned to \( P_{ij} \) and \( P_{ij} \) be reserved for \( P_{ij} \) and \( P_{ij} \). If there is no newly accepted traffic demand from input \( p \) to output \( j \), the time slot just reserved for \( P_{ij} \) will make \( v_{ij} \) negative. After updating these two switching matrices, \( V \) is updated such that each line sum of \( V \) is still kept zero. The above procedure is repeated until \( V \) is full of zero entries. Then the resulting sequence of the switching matrices is an optimal schedule of \( T'' \) and hence a schedule of \( T \).

In the following, we shall first describe the reallocation algorithm.

#### B. Reallocation Algorithm

The reallocation algorithm essentially first deallocates \( P_{ij} \) from \( S_r \) and \( P_{ij} \) from \( S_r \), and allocates \( P_{ij} \) in \( S_r \). Then the packet in conflict with \( P_{ij} \) is reallocated from \( S_r \) to \( S_r \). This reallocation may further introduce a conflict in \( S_r \). Then the packet in conflict with the reallocated packet is reallocated from \( S_r \) to \( S_r \). This procedure is repeated until no conflict exists. \( P_{ij} \) is finally allocated. The reallocation algorithm is given as follows:
Reallocation Algorithm

Input: \(i, j, p, q, i \neq p \) and \( j \neq q \), and two switching matrices\( S_i \) and \( S_j \), \( x \neq y \), where \( S_i \) contains an entry \( l_{ij} \) and \( S_j \) contains an entry \( l_{pq} \).

Output: Two switching matrices \( S_i \) and \( S_j \) resulting from updating \( S_i' \) and \( S_j' \), respectively, such that \( S_i + S_j - S_i' - S_j' = [ -1 ]_{ij} + [ -1 ]_{pq} + [ 1 ]_{ij} + [ 1 ]_{pq} \).

Procedure:

Step 1. \( S_i \leftarrow S_i' \).
Step 2. \( s_{xq} \leftarrow 0. \) /* Deallocate \( P_{xq} \) from \( S_j \). */
Step 3. \( s_{yq} \leftarrow 0. \) /* Deallocate \( P_{yq} \) from \( S_j \). */
Step 4. \( s_{yq} \leftarrow 1. \) /* Allocate \( P_{yq} \) in \( S_j \). */
Step 5. /* If \( k = j \), then allocate \( P_{xq} \) in \( S_j \). */
Step 6. /* Find \( P_{xq} \) in column \( k \) of \( S_j \) that is in conflict with \( P_{xq} \), and reallocate it from \( S_j \) to \( S_i \). */
Step 7. /* If \( m = p \), then allocate \( P_{yq} \) in \( S_j \). */
Step 8. \( g \leftarrow m. \)

We have proved the following theorem in [5].

Theorem 1: After applying the reallocation algorithm, the matrices \( S_i \) and \( S_j \) resulting from updating \( S_i' \) and \( S_j' \), respectively, are still switching matrices, and \( S_i + S_j - S_i' - S_j' = [ -1 ]_{ij} + [ -1 ]_{pq} + [ 1 ]_{ij} + [ 1 ]_{pq} \).

Thus, the output of the reallocation algorithm is correct.

C. Adaptive Scheduling Algorithm

We now give the detailed adaptive scheduling algorithm in the following.

Adaptive Scheduling (AS) Algorithm

Input: (1) A traffic matrix \( T \), which satisfies the scheduling criteria in a frame of length \( L \), (2) a scheduled traffic matrix \( T' \) which is complete with respect to \( L \), and (3) an optimal schedule \( \{ S_i' \} \) of \( T' \), where \( T' \) and \( T \) are traffic matrices of two consecutive frames and the frame corresponding to \( T' \) comes before the frame corresponding to \( T \).

Output: An optimal schedule \( \{ S_i \} \) of \( T \).

Procedure:

Step 1. By calling the filling algorithm [4], add dummy traffic to \( T \) such that \( T \) is complete with respect to \( L \).
Step 2. \( V \) is initiated with \( T - T' \), and \( \{ S_i \} \) is initiated with \( \{ S_i' \} \). The entry \( r_{ij} \) of the column vector \( R \) is initiated with the sum of negative entries in row \( i \) of \( V \) for all \( i, 1 \leq i \leq N \).
Step 3. \( i \leftarrow 1. \)
Step 4. /* At the following three steps, find a traffic demand which is no longer needed in the current frame if it exists. */
Step 5. /* Find the first column \( j \) such that \( v_{ij} < 0. \)
Step 6. /* Find a new traffic demand in the current frame from the same input line as the traffic demand found at Step 4. */
Step 7. /* Find a traffic demand which is destined for the same output line as the traffic demand found at Step 7 and is no longer needed in the current frame. */
Step 8. /* Find two time slots containing the traffic demands found at Step 6 and Step 8. */
Step 9. /* Find a switching matrix \( S_i \) such that it contains an entry \( l_{ij} \). */
Step 10. If \( x = y \) Begin
Step 11. \( s_{xq} \leftarrow 0. \) /* Deallocate \( P_{xq} \) in \( S_i \). */
Step 12. \( s_{yq} \leftarrow 1. \) /* Allocate \( P_{yq} \) in \( S_i \). */
Step 13. End
Else
End
Step 11. /* Update $R^-$ and $V$. */

\[ r_j^- \leftarrow r_j^- + 1. \]

/* Note that $P_{p,q}$ in $S_j$ is deallocated at Step 10. Hence the number of traffic demands from input $p$ which can be deallocated should be decreased by 1. That is, $r_p^-$ should be increased by 1. But note that $P_{p,q}$ is also deallocated at Step 10. If $v_{p,q} \leq 0$, i.e., there is no new traffic demand from input $p$ to output $q$, then the just allocated $P_{p,q}$ shall be deallocated and hence $r_p^-$ should be decreased by 1. So, if $v_{p,q} \leq 0$, $r_p^-$ is unchanged. */

If $v_{p,q} > 0$ then

\[ r_p^- \leftarrow r_p^- + 1. \]

\[ V \leftarrow V + [[1], [1], [1], [1], [1]]^T \]

Go To Step 4.

The following theorems have been proved in [5].

**Theorem 2:** Updating of $V$ at Step 11 is correct, and each line sum of updated $V$ is kept zero.

**Theorem 3:** $V$ is full of zeros when the AS algorithm terminates.

Hence we know that the AS algorithm will update the switching matrices such that the sequence of these updated switching matrices is a schedule of the given traffic matrix.

### D. Time Complexity

The data structures used to maintain the switching matrices may affect the time complexities of the AS algorithm. We shall analyze it together with those data structures used to reduce the time spent in searching for data structures when we analyze the time complexity of the algorithm. We shall analyze it together with those data structures used to reduce the time spent in searching for data structures used to reduce the time spent in searching for $\{S_x, S_y\}$ when searching for $k$ at Step 4 and $m$ at Step 6 in the reallocation algorithm. Note that the data structures described in the following are only initiated when the switching system is set up. Therefore we do not take into account of the time to set up the data structures when we analyze the complexity of the AS algorithm.

Consider the reallocation algorithm first. Since a switching matrix $S$ is obviously a permutation matrix [18], we can use a two-dimensional array $SM$ to represent the sequence of $L$ switching matrices $\{S\}$. $SM(x,i) = j$ if $s_{x,i} = 1$.

So searching for $k$ at Step 4 can be done by the following statement:

\[ k \leftarrow SM(v,g), \text{ in constant time.} \]

In addition, another two-dimensional array $ISM$ is also used to represent the same sequence of $L$ switching matrices $\{S\}$. $ISM(x,j) = i$ if $s_{x,j} = 1$. So searching for $m$ at Step 6 can be done by the following statement:

\[ m \leftarrow ISM(x,k), \text{ in constant time.} \]

The detailed implementation for the reallocation algorithm by using these data structures is not presented in this paper, but it is easy to see that the reallocation of a packet requires updating of the entries of $SM$ and $ISM$ in constant time. Consequently the time complexity of the reallocation algorithm is $O(N)$ since there are at most $2N$ reallocations. Simulation results given in the next section will show that the number of reallocations within the reallocation algorithm is far less than $2N$.

Now consider the AS algorithm. In order to search for $S_x$ and $S_y$ at Step 9 in the AS algorithm in constant time, data structures of doubly linked lists [12] are used. For any unity entry, there are two linking fields, $PRE$ and $SUCC$, associated with it. The $PRE$ field of a unity entry, say $s_{xa}$, contains the index of the preceding switching matrix that contains $1_{xa}$. The $SUCC$ field of $s_{xa}$ contains the index of the succeeding switching matrix that contains $1_{xa}$. The headers of these linked lists are represented by a two-dimensional array $LL\_FIRST$ of size $N \times N$. All the switching matrices containing an entry $1_{xa}$ are linked together by the doubly linked list with the header $LL\_FIRST(a,b)$, for all $a$ and $b$, $1 \leq a, b \leq N$. When we search for a switching matrix $S_x(S_y)$ that contains an entry $1_{uv}(1_{vy})$ at Step 9, the first switching matrix pointed to by $LL\_FIRST(u,v)$ ($LL\_FIRST(v,y)$) is chosen and is easily found in constant time. The updating of these linked lists is performed when packets are reallocated at Step 10.

When a packet $P_{a,b}$ is deallocated from its corresponding switching matrix, say $S_x$, $S_y$ needs to be deleted from the linked list with the header $LL\_FIRST(a,b)$. Since it is a doubly linked list, it is easy to delete $S_x$ in constant time by updating $SUCC(PRE(x,a),a)$ and $PRE(SUCC(x,a),a)$ [12].

When a packet $P_{a,b}$ is allocated to $S_x$, $S_y$ needs to be inserted into the linked list with the header $LL\_FIRST(a,b)$. It is simply inserted to the head of this linked list by updating $PRE(LL\_FIRST(a,b),a)$, $LL\_FIRST(a,b)$, and $SUCC(x,a)$ in constant time [12]. Consequently the updating of these linked lists can be done in constant time.

After describing the data structures, we consider the time complexity of each step of the AS algorithm. The preprocessing at Step 1 and Step 2 has time complexity $O(N^2)$. The tasks to search for $i$, $j$, $p$, and $q$ from Step 4 to Step 8 have time complexity $O(N)$. Searching for $S_x$ and $S_y$ at Step 9 can be done by searching linked lists in constant time.

The time complexity of Step 10 is dominated by that of the reallocation algorithm and hence is $O(N)$. Finally, consider the number of iterations of the procedure. At each iteration, the sum of the positive entries in the matrix $V$ will be decreased by at least 1 (and the sum of the negative entries in the matrix $V$ will be incremented by at least 1), so the number of iterations is at most equal to the sum of the positive entries, denoted as $\delta$, in $V' = V - T'$ at Step 2. As a result, the AS algorithm has the time complexity $O(max(N^2, \delta N))$.

In the worst case, all the traffic demands from a given input line are changed such that the sum of the positive entries in each row of $V'$ at Step 2 is the frame length $L$. Hence the maximum value of $\delta$ is $NL$. Consequently, the AS algorithm has the worst case time complexity $O(N^2 L)$.

Comparing the AS algorithm of time complexity $O(N^2 L)$ with previous time slot assignment algorithms of time complexity $O(N^4 L)$, the algorithm may perform worse than previous time slot assignment algorithms if $L > cN^{3/2}$, where $c$ is a parameter depending on the hardware (the network controller, the switch architecture, the machine wherein
algorithms are implemented, etc.) and the software (the implementations of algorithms, etc.). In other words, the AS algorithm will perform better than previous time slot assignment algorithms if \( L < c N^{2.5} \). Consequently the AS algorithm can perform better than previous time slot assignment algorithms when \( N \) is large and/or \( L \) is small in the worst case.

Since, in many applications, most of connections will continue for a number of consecutive frames, the amount of changes of traffic demands \( \delta \) will be far less than \( NL \), and hence the number of total reallocations will be far less than \( 2N^2 L \), the maximum number of total reallocations. So the AS algorithm is expected to be faster than previous time slot assignment algorithms in many practical applications.

IV. Probability Model and Simulation Results

The time complexity of the AS algorithm is dominated by the number of total reallocations which depends on the amount of changes of traffic demands \( \delta \) and the number of reallocations within the reallocation algorithm. A probability model with interdependent traffic is used to analyze the amount of changes of traffic demands \( \delta \). This model is validated by computer simulations. The number of reallocations within the reallocation algorithm is obtained through simulations.

A. Probability Model

The connection requests from an input line are assumed to be generated according to a Poisson distribution with mean \( \lambda \), measured in number of connection requests per time slot, and the connection holding time is assumed to be exponentially distributed with mean \( 1/\mu \), measured in number of time slots per connection. The probability that a connection request is destined for a given output line is \( \lambda/N \). Hence the connection requests from input \( i \) to output \( j \) are generated according to a Poisson distribution with mean \( \lambda N^j \). For convenience, let

\[
\lambda^i = \lambda i / N. \tag{1}
\]

Since we assume the probability that a connection request is destined for any given output is identical, the expected capacity from a given input to a given output is \( \lambda N^j \) time slots. So, as the ratio \( L/N \) becomes large, correlation caused by enforcement of the scheduling criteria among inputs and outputs becomes negligible. This implies that, as \( L/N \) becomes large, \( t_{ij} \) can be viewed as an independent probability random variable, where \( t_{ij} \) is the number of traffic demands from input \( i \) to output \( j \). For small \( L/N \), the correlation among inputs and outputs can not be neglected. Due to this correlation, the number of unusable time slots will increase

[22]. Hence the number of accepted connection requests will be less than the expected number of traffic demands when \( L/N \) is small. This implies that the expected amount of changes of traffic demands \( E[\delta] \), obtained based on the assumption of independence among inputs and outputs, can give an upper bound for actual \( \delta \) on the average.

Suppose \( T \) and \( T' \) are two consecutive traffic matrices and the frame corresponding to \( T' \) precedes the frame corresponding to \( T \). Let \( V = T - T' \) and let

\[
v'_{i,j} = \begin{cases} v_{i,j} - t'_{i,j} & \text{if } v_{i,j} \geq t'_{i,j} \\ 0 & \text{otherwise.} \end{cases} \tag{2}
\]

Since the time taken by the AS algorithm depends on the sum of the positive entries in \( V \), we shall find the mean value of the sum of the positive entries in \( V \), \( E[\delta] \), which is given as follows:

\[
E[\delta] = N^j E[v'_{i,j}] = N^j \sum_{i=0}^{L} \sum_{k=0}^{\infty} k \text{Pr}[v'_{i,j} = n + k | t'_{i,j} = n], \tag{3}
\]

as \( L/N \) becomes large.

Assume that the switching system is stationary. So the limiting probabilities \( \text{Pr}[t'_{i,j} = n] = \text{Pr}[t_{i,j} = n] \) for all \( i, j \), and \( E[\delta] \) is small. This implies that the excepted amount of changes of traffic demands \( E[\delta] \), obtained based on the assumption of independence among inputs and outputs, can give an upper bound for actual \( \delta \) on the average.

In the following, we shall derive the transition probabilities \( q_{n,m} \), and hence the limiting probabilities \( \pi_m \) can be obtained from (4) and (5). Finally \( E[\delta] \) is obtained from (6).

The probability that, given \( t_{i,j} = n \), \( m \) connections will be disconnected in the frame corresponding to \( T \) is

\[
\left( \frac{n}{m} \right) (1 - e^{-\mu L})^m (e^{-\mu L})^{n-m}, \quad 0 \leq m \leq n. \tag{7}
\]

The probability that, as \( L/N \) becomes large, there are \( m+k \) connection requests from input \( i \) to output \( j \) generated in the frame corresponding to \( T \) is

\[
\frac{X^m L^k e^{-XL}}{(m+k)!}, \quad m + k \geq 0. \tag{8}
\]

Hence, \( q_{n,m+k} \) is computed from (9) as follows:

\[
q_{n,m+k} = \begin{cases} \sum_{m=\max(-k,0)}^{n} \left( \frac{n}{m} \right) (1 - e^{-\mu L})^m (e^{-\mu L})^{n-m} \frac{(X^m L^k e^{-XL})^{m+k} e^{-XL}}{(m+k)!} & \text{if } 0 \leq n+k < L \\ 1 - \sum_{m=0}^{n} q_{n,m} & \text{if } n+k = L. \end{cases} \tag{9}
\]
Simulations are conducted to test the validity of the probability model with various values of \( L \) and \( N \). Given \( N = 20, \lambda = 0.1, \) and \( \mu = 0.1 \), the analytic and simulation results are the same with \( L \geq 200 \), as shown in Fig. 2. With \( L \leq 200 \), the analytic and simulation results are not the same but they are very close such that their values are almost equal in logarithmic scale. Note that, with \( L \leq 200 \), the analytic result actually gives an upper bound for \( E[\delta] \). Given \( L = 300, \lambda = 0.1, \) and \( \mu = 0.1 \), the analytic and simulation results are the same with \( N \leq 60 \), as shown in Fig. 3. With \( N > 60 \), the analytic and simulation results are not the same but they are also very close such that their values are almost equal in logarithmic scale. Note that, with \( N > 60 \), the analytic result also gives an upper bound for \( E[\delta] \). These facts indicate that the probability model is useful for measuring the maximum number of changes of traffic demands \( \delta \) on the average. Note also that the average value of \( \delta \) is far less than that of the worst case as derived in Section III.

### B. Simulation Results

Simulations are conducted to measure the number of reallocations within the reallocation algorithm, and the interframe dependency.

Fig. 4 shows the simulation results of the average number of reallocations in a call of the reallocation algorithm with various values of \( N \) and \( \mu \). The results show that it is proportional to \( N \) and far less than the number of reallocations in the worst case.

Since we assume that the connection holding time is exponentially distributed with mean \( 1/\mu \) time slots per connection, the expected number of frames in which a connection lasts is equal to \( 1/\mu L \). Hence we can use \( 1/\mu L \) to measure the interframe dependency. Fig. 5 shows the average value of \( \delta \) as a function of \( 1/\mu L \) with various values of \( \lambda \).

The average value of \( \delta \) decreases as \( 1/\mu L \) becomes large since most of traffic demands in a given frame are the same as those in the preceding frame. Since the probability values calculated by (7) approach zero except \( m = 0 \) as \( 1/\mu L \) becomes large, the average values of \( \delta \) approach some constants, respectively. This implies that \( \delta \) will be saturated. This is verified by the simulation results shown in Fig. 5. Meanwhile we can see that the average value of \( \delta \) is small with interdependent traffic (i.e., \( 1/\mu L > 1 \)). That is, the AS algorithm is efficient for interdependent traffic.

### V. CONCLUSIONS

In previous works, the interdependence between traffic demands in two consecutive frames is neglected for scheduling time slots in a time-division multiplexed switching system. Their approaches to the scheduling problem for avoiding output conflicts are resorted to some combinatorial optimization methods for finding a sequence of matchings. As a result, previous scheduling algorithms have time complexities \( O(N^4) \) or \( O(N^5) \). However, in this paper, an adaptive scheduling algorithm AS of the worst case time complexity \( O(N^2 L) \) is proposed. Comparing the time
complexity of the AS algorithm with those of previous time slot assignment algorithms, the AS algorithm can perform better than previous time slot assignment algorithms when $N$ is large and/or $L$ is small. More precisely, since the interdependence between traffic demands in two consecutive frames is considered, the performance of the AS algorithm is dependent on the amount of changes of traffic demands. Since the traffic demands in consecutive frames are expected to be interdependent in many practical applications, the AS algorithm may offer as an efficient alternative for scheduling time slots in these applications.

REFERENCES


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